

Fig. 1 system diagram.

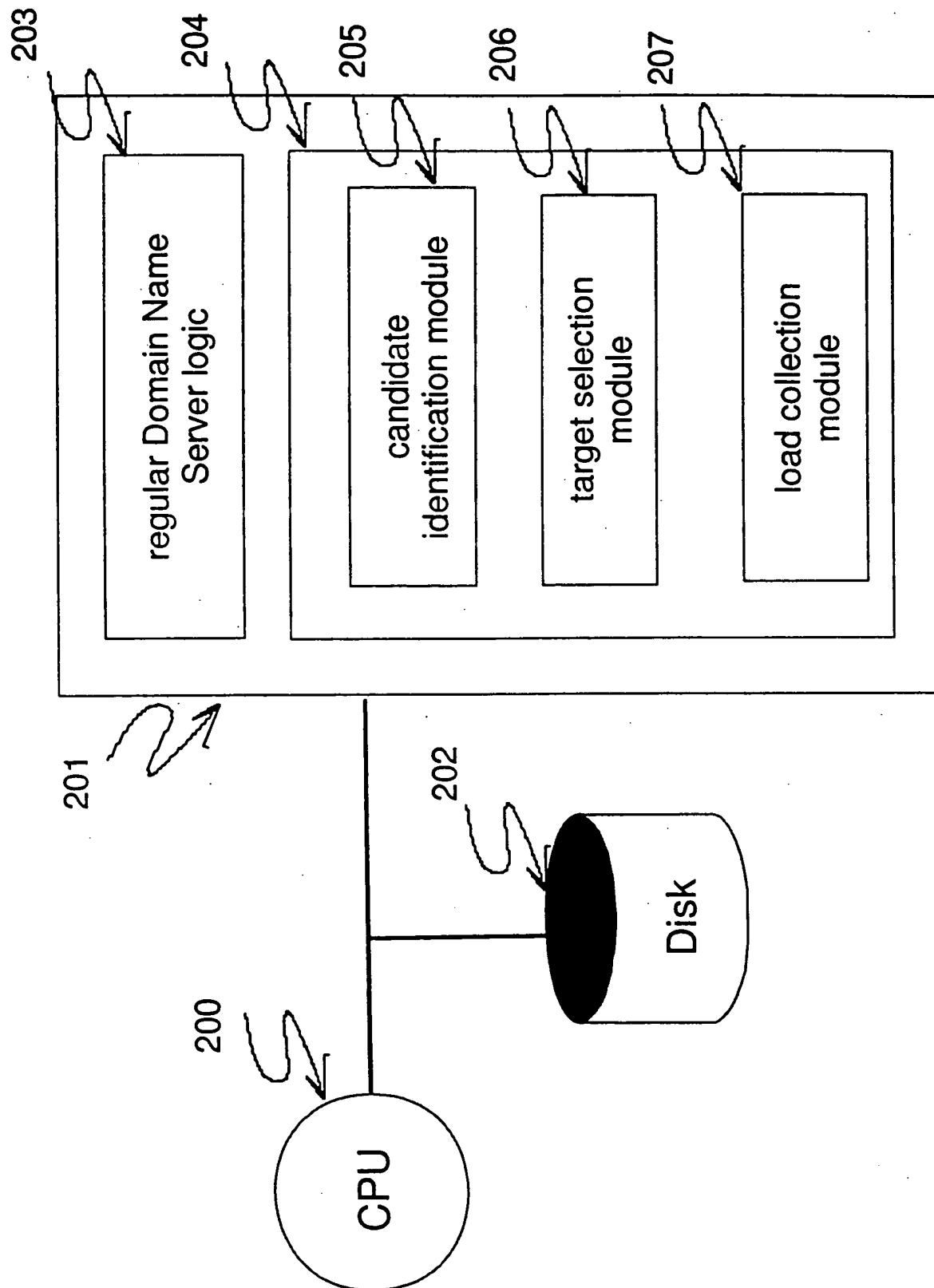


Fig. 2.

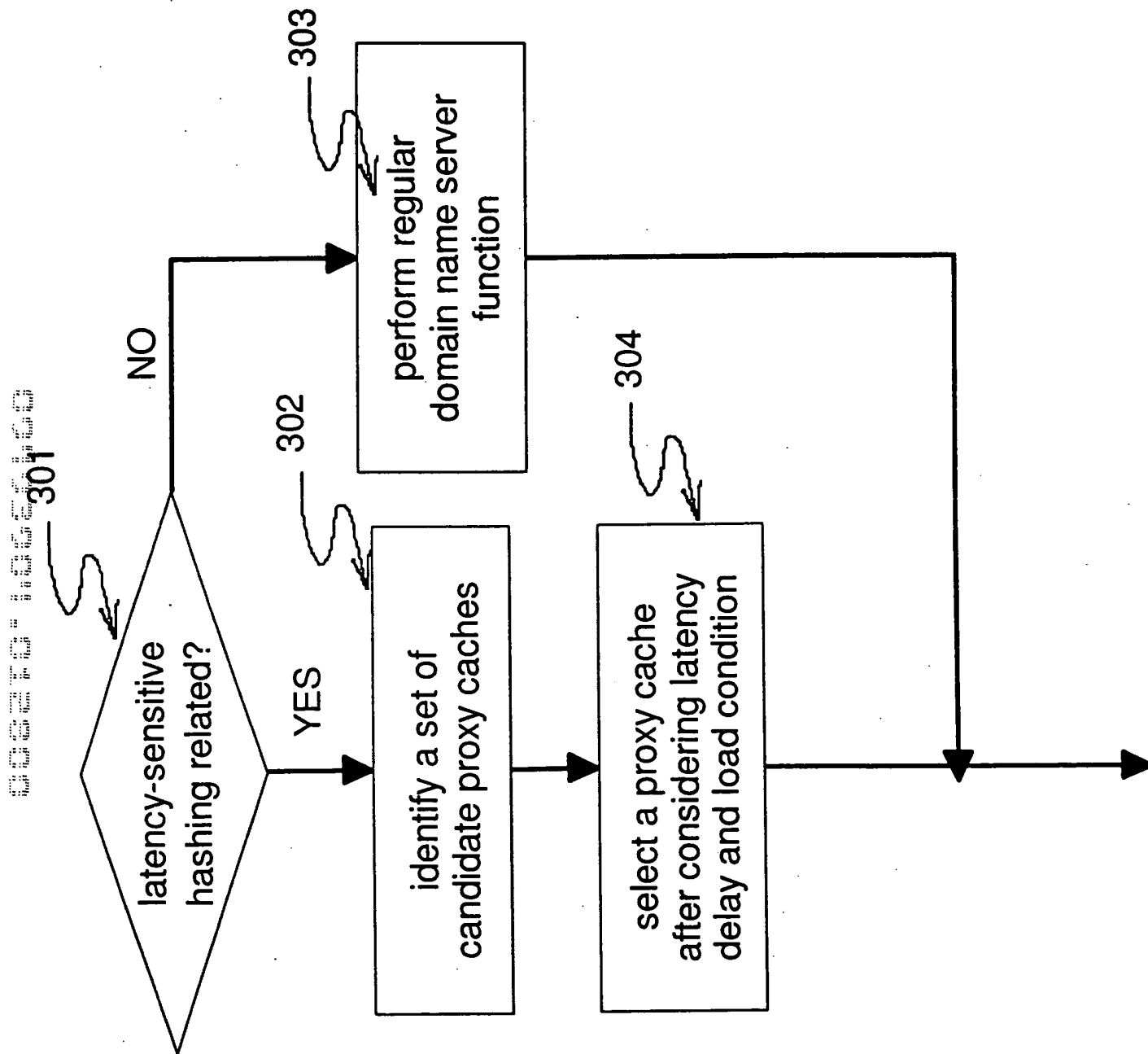


Fig. 3

Fig. 4

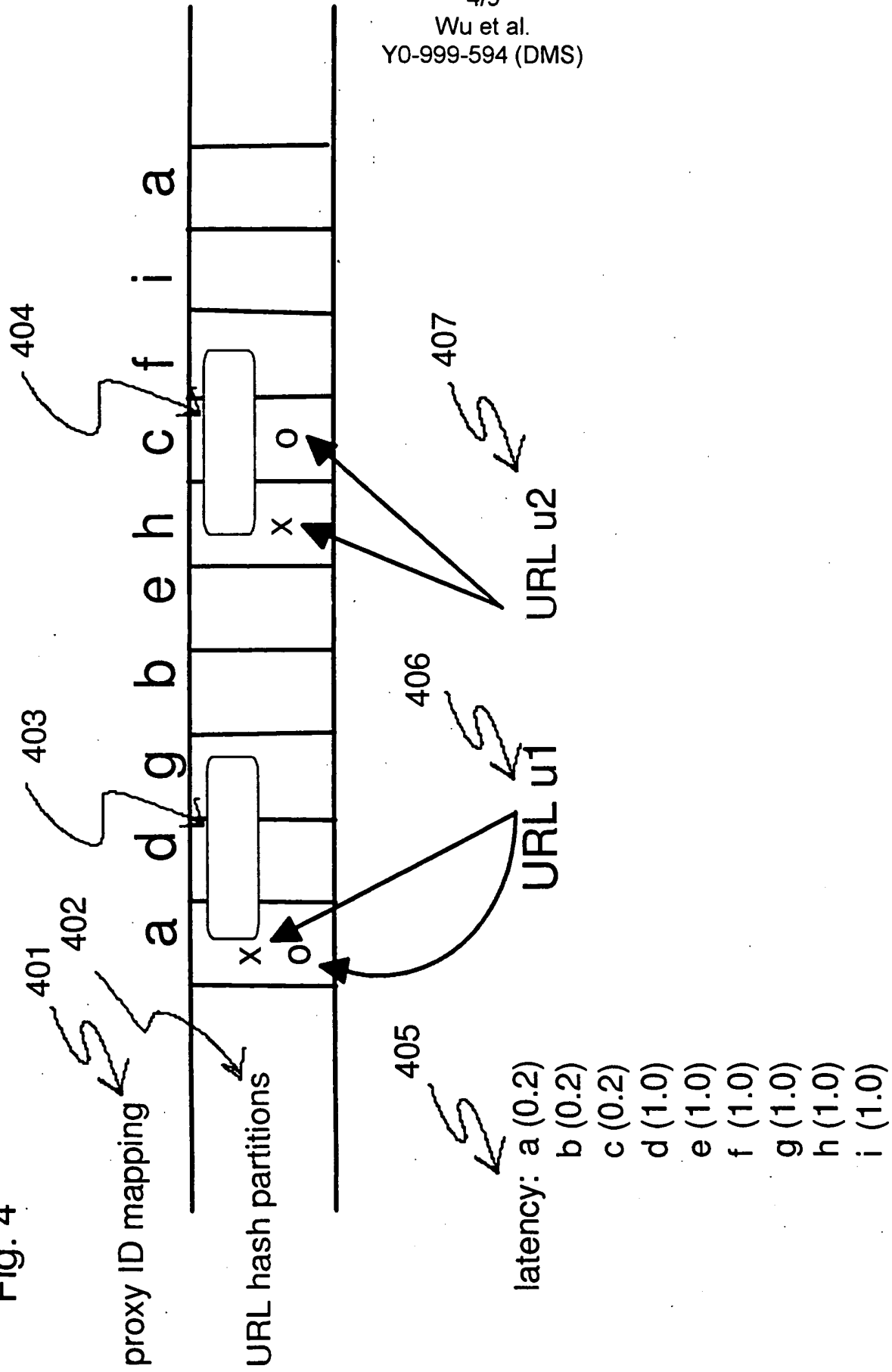


Fig. 5

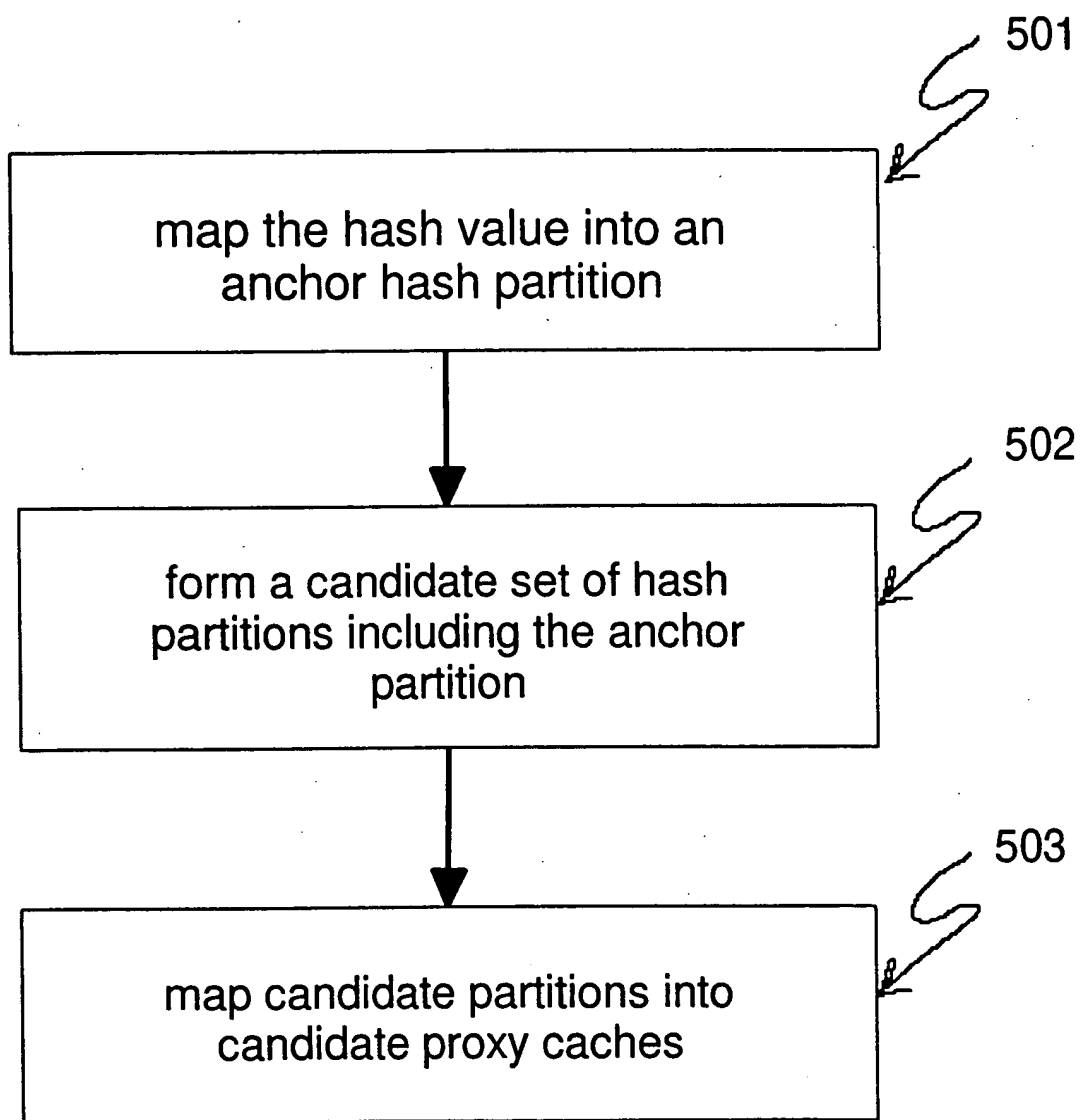
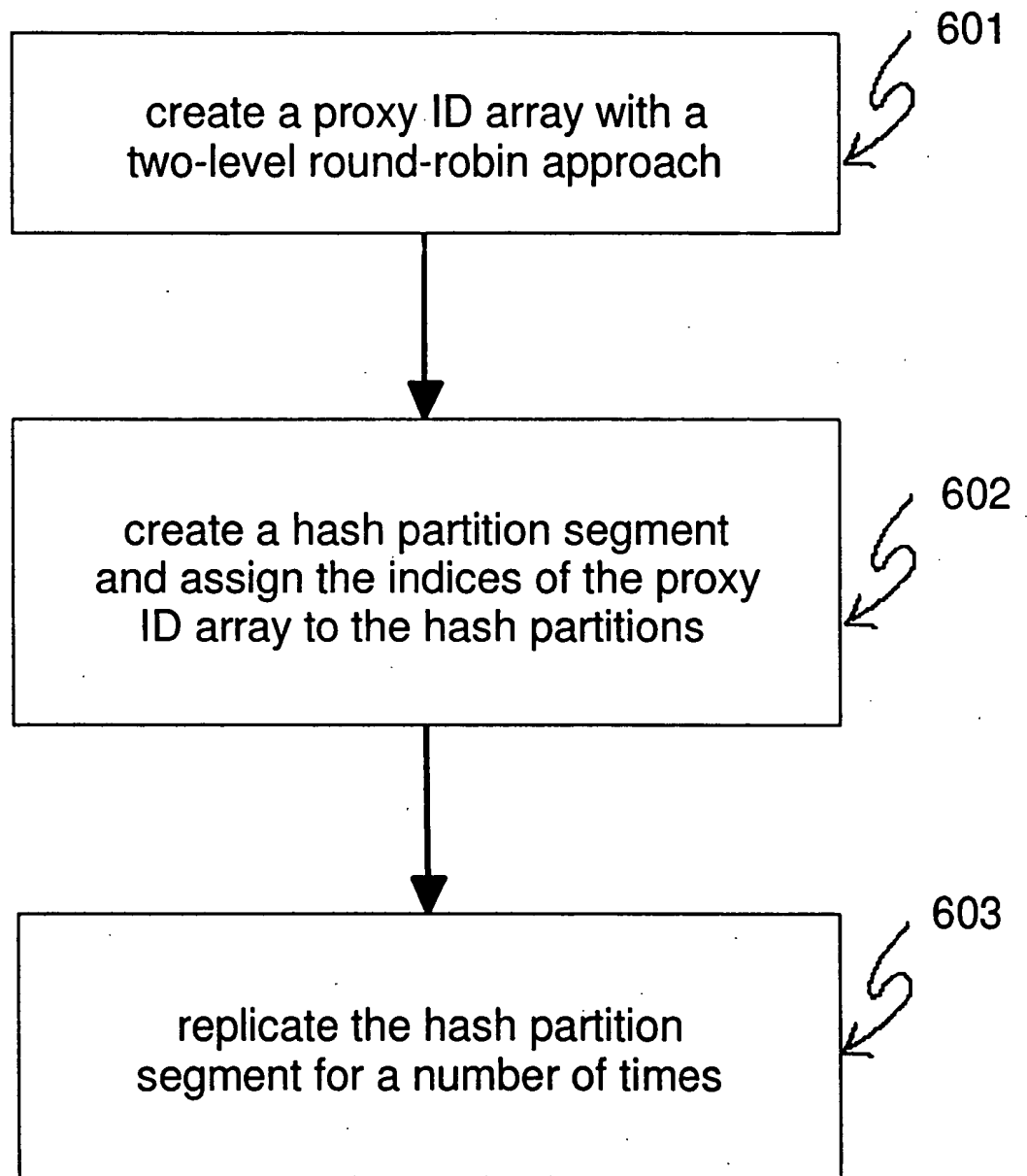
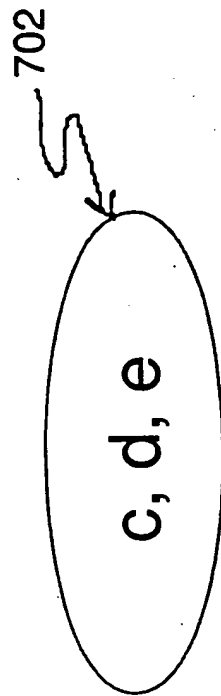
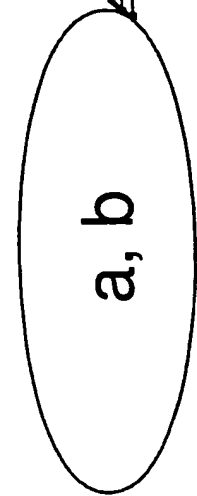


Fig. 6



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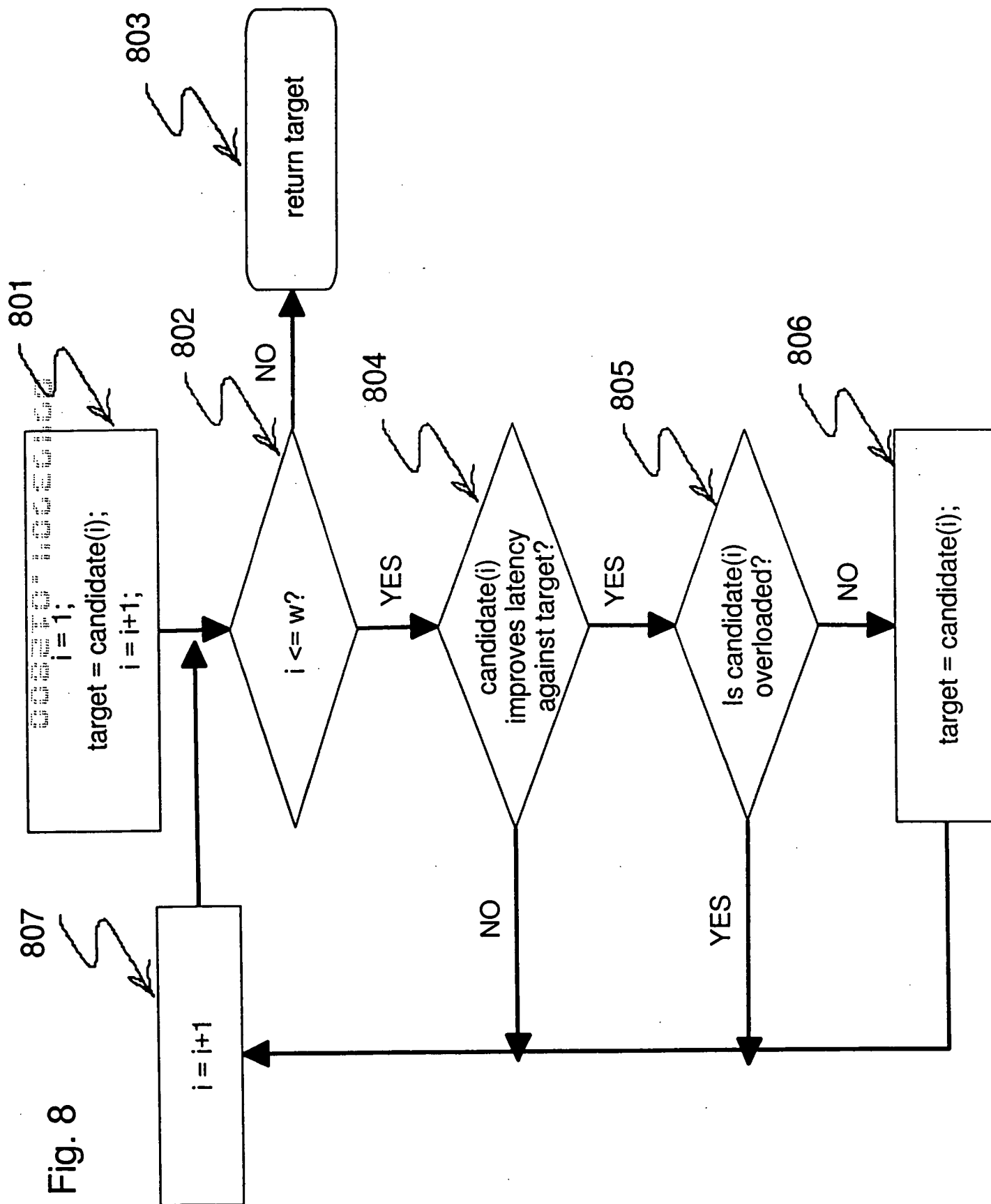


proxy ID array

a	c	b	d	a	e	b	c	a	d	b	e
0	1	2	3	4	5	6	7	8	9	10	11

hash partition segment

Diagram illustrating a mapping from a 16-bit input vector to an 8-bit output vector. The input vector (top) has bits: 0, 0, 4, 4, 4, 8, 8, 2, 2, 6, 6, 10, 10, 1, 1, 1. The output vector (bottom) has bits: 7, 7, 3, 3, 3, 9, 9, 5, 5, 11, 11, 11, 11, 11. A long arrow points from the input vector to the output vector, indicating a transformation.



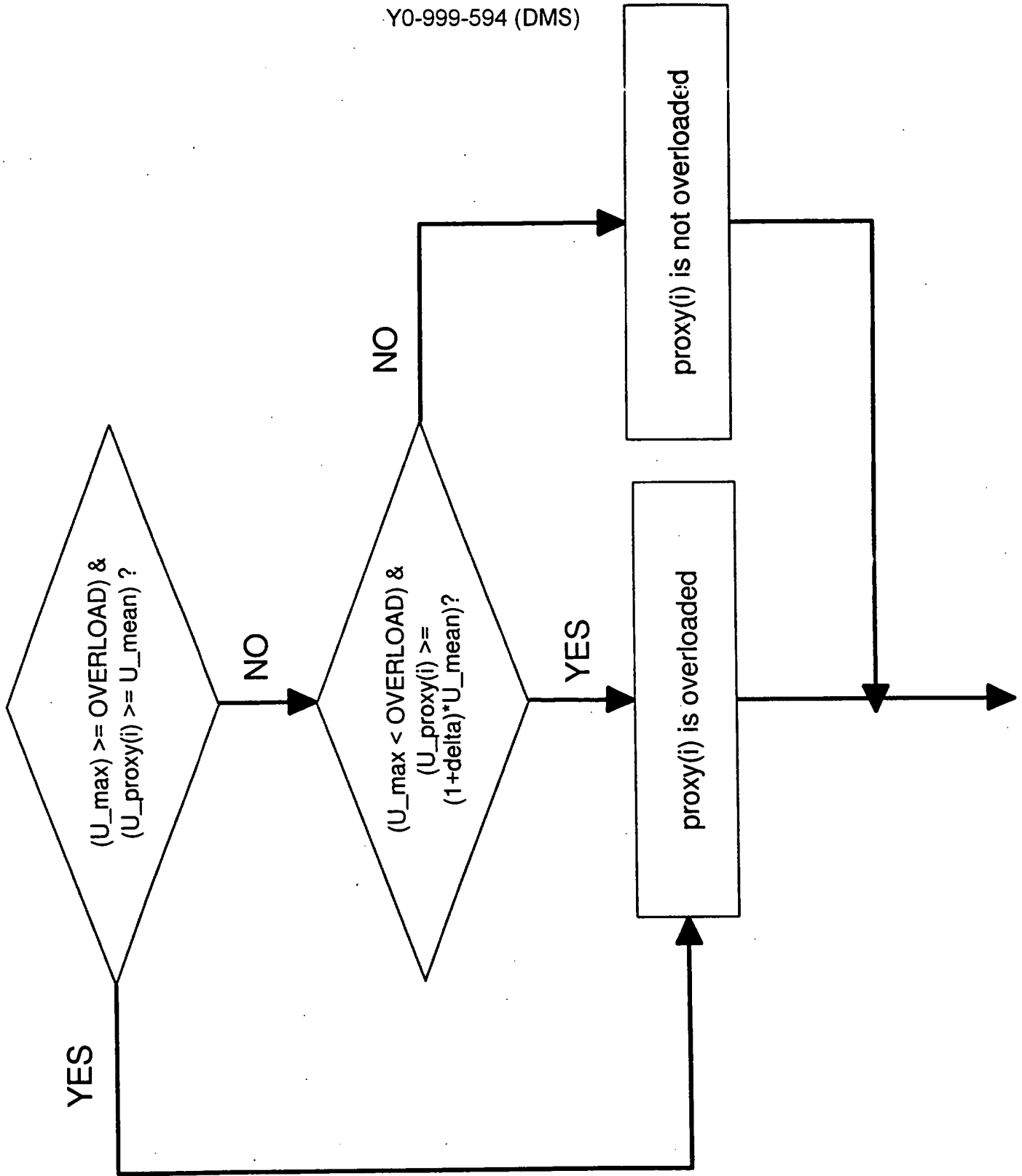


Fig. 9